a transmit datapath corresponding to each data station coupled between said transmit memory means and said communications medium for providing at least some data from said transmit memory means to said communications medium;

each said receive datapath including;

a deserializer configured to receive serial data from said communications medium and output at least a portion of said received serial data in parallel;

means for selectively transmitting, in response to one of said plurality of control signals, said data output by said deserializer to said <u>receive</u> memory means;

<u>each</u> said transmit datapath including a serializer configured to receive parallel data and output serial data.

14.   (Amended)   In a data communication network for communicating between data stations over a communications medium under control of a processor which outputs a plurality of control signals, apparatus comprising:

a receive memory device and a transmit memory device;

a <u>plurality of</u> receive [datapath] <u>datapaths</u> for providing at least some data received over said media to said receive memory device;

a <u>plurality of</u> transmit [datapath] <u>datapaths</u> for providing at least some data from said transmit memory device to said communications medium;

<u>each of</u> said receive [datapath] <u>datapaths</u> including

a deserializer configured to receive serial data and output at least a portion of said received serial data in parallel;